

**EXPEDITED PROCEDURE – EXAMINING GROUP 2192**

**S/N 10/676,141**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Peramachanahalli S. Ramkumar et al.	Examiner:	Chrystine Pham
Serial No.:	10/676,141	Group Art Unit:	2192
Filed:	September 30, 2003	Docket No.:	884.A45US1
Title:	COMBINED EMULATION AND SIMULATION DEBUGGING TECHNIQUES		
Customer Number:	21186		

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**AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116**

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In response to the Final Office Action mailed April 17, 2007, please amend the application as follows: